Notes:

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ISTRUMENTS Spartan 6 LX150T Reference Design A/T 12V Input Bus Design. VS1 12V@112W #1 Sequence Voltage (First on) Core PTH08T230W 1.2V@3A **VCC**<sub>INT</sub> 1.2V@6A  $5k\Omega$ Eff. 83% TPS3808G0.9 /Reset -Vcc 5V-1V SVS A/T VS2 Delay VCC to #2 Sequence control logic high SVS PTH08T230W I/O VCC<sub>AUX</sub> 3.3V@3A-3.3V@6A Eff. 92% ≥Vcc 5V TPS3808G25 /Reset(All other voltages) A/T 2.5V SVS VS3 Delay PTH08T260W DDR3 VDD, -1.5V@2A 1.5V@3A VCCO Eff. 87% VCC LDO VL1 TPS51200 -DDR3 0.75V@2A-From SVS2.5V 0.75 @3A DDR3 VTT /Reset(All other voltages) Eff. 60% 5.1V VS4 VA/T **GTP Core** PTH08T230W 1.2V@3A 1.2V@6A Low noise 5mV ripple Eff. 85% VS5 **GTP Termination** PTH08T230W 1.2V@3A-Low noise 1.2V@6A 5mV Ripple Eff. 85% VS6 **FMC Connector** PTH08T230W -3.3V,2.5V,1.8V @4A selectable 3.3,2.5,1.8 V@6A (2A) FPGA I/O Eff.92% Vadjust Voltage selectable VS7 with jumper PTH08T230W **FMC Connector** 3.3,2.5,1.8 V@6A 3.3V,2.5V,1.8V @4A selectable (2A) FPGA I/O Eff.92% Voltage selectable VS8 with jumper PTH08T230W 3.3V@6A FMC Connector (2 3.3V@6A Eff. 92% x3A) V<u>S9</u> PTH08080W 5V@1A **ALI Connector** 5V@1A Eff.93%

12V@2A

**FMC Connector** 

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