

1

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Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Page 2

BLOCK DIAGRAM

Page 3

Power sequencing, Reset, WDT

Page 4

Threshold detection

Orderable: N/A

TID #: 010041

Number: TIDA-010041

SVN Rev: Not in version control

Drawn By:

Engineer: Harsh Misra

Designed for: Public Release

Project Title: TIDA-010041

Sheet Title:

Assembly Variant: 001

File: TIDA-010041-E1_CoverSheet.SchDoc

Contact: http://www.ti.com/support

Mod. Date: 10/2/2018

Sheet: 1 of 5

Size: B

TEXAS INSTRUMENTS

http://www.ti.com

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1

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A

B

C

D

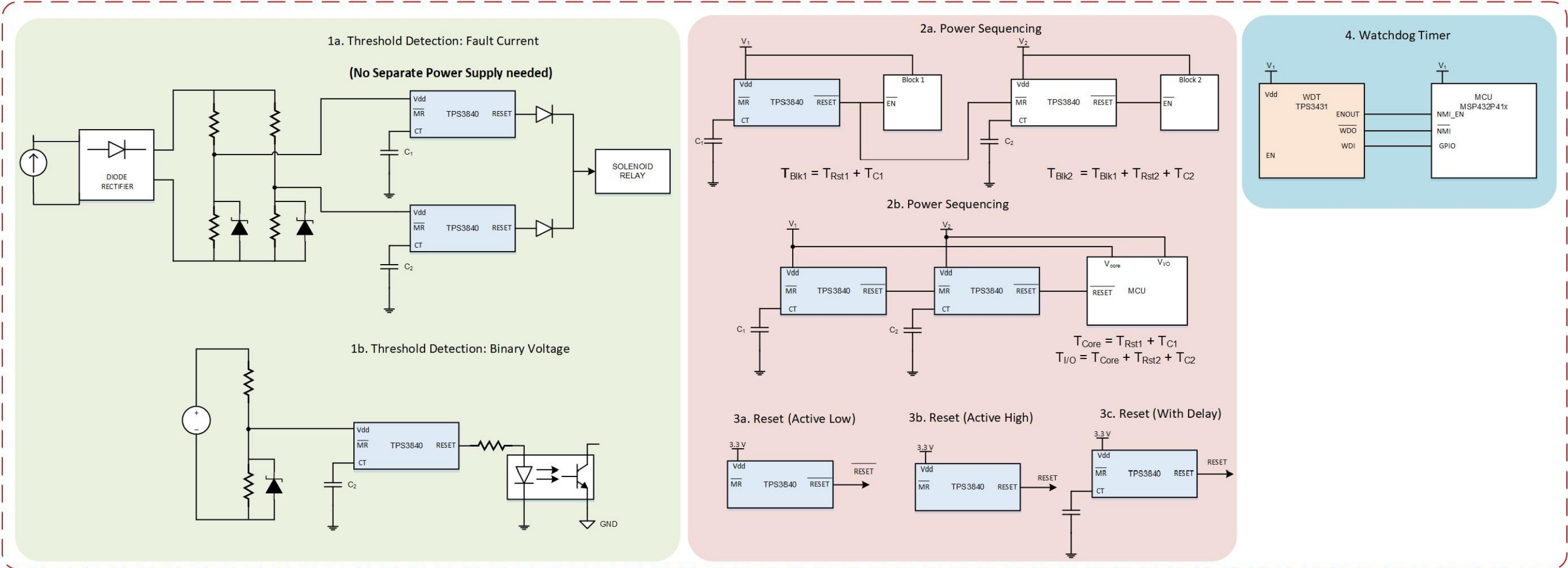
A

B

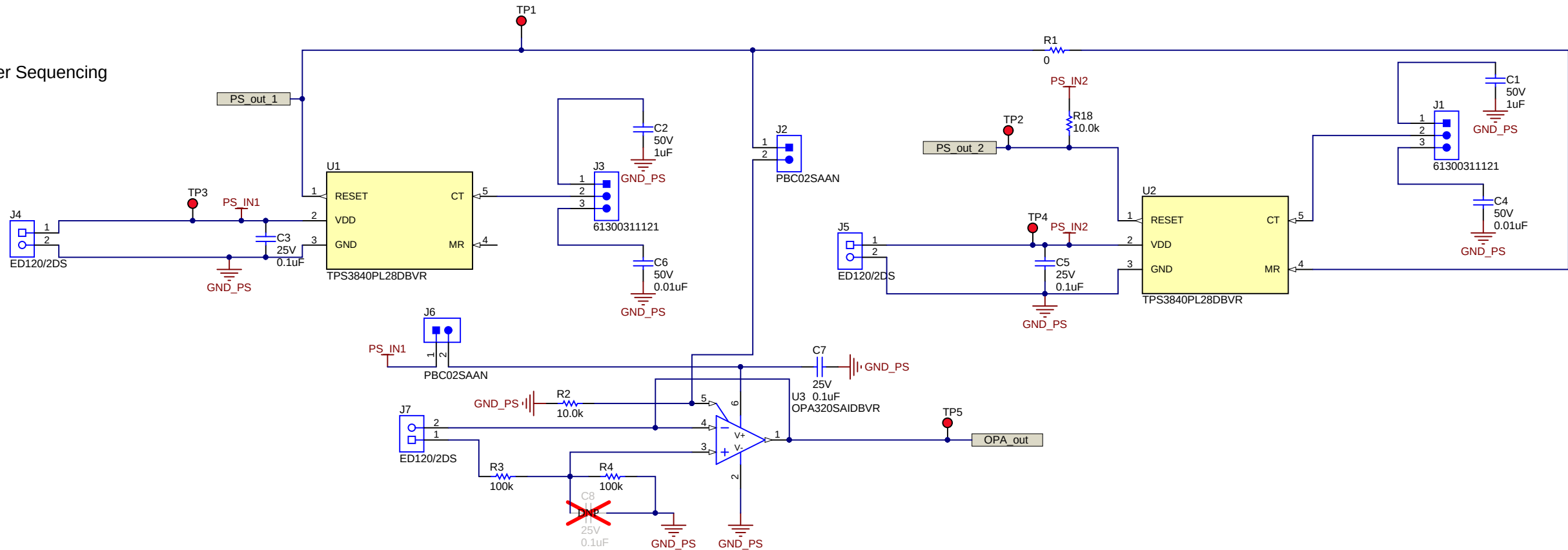
C

D

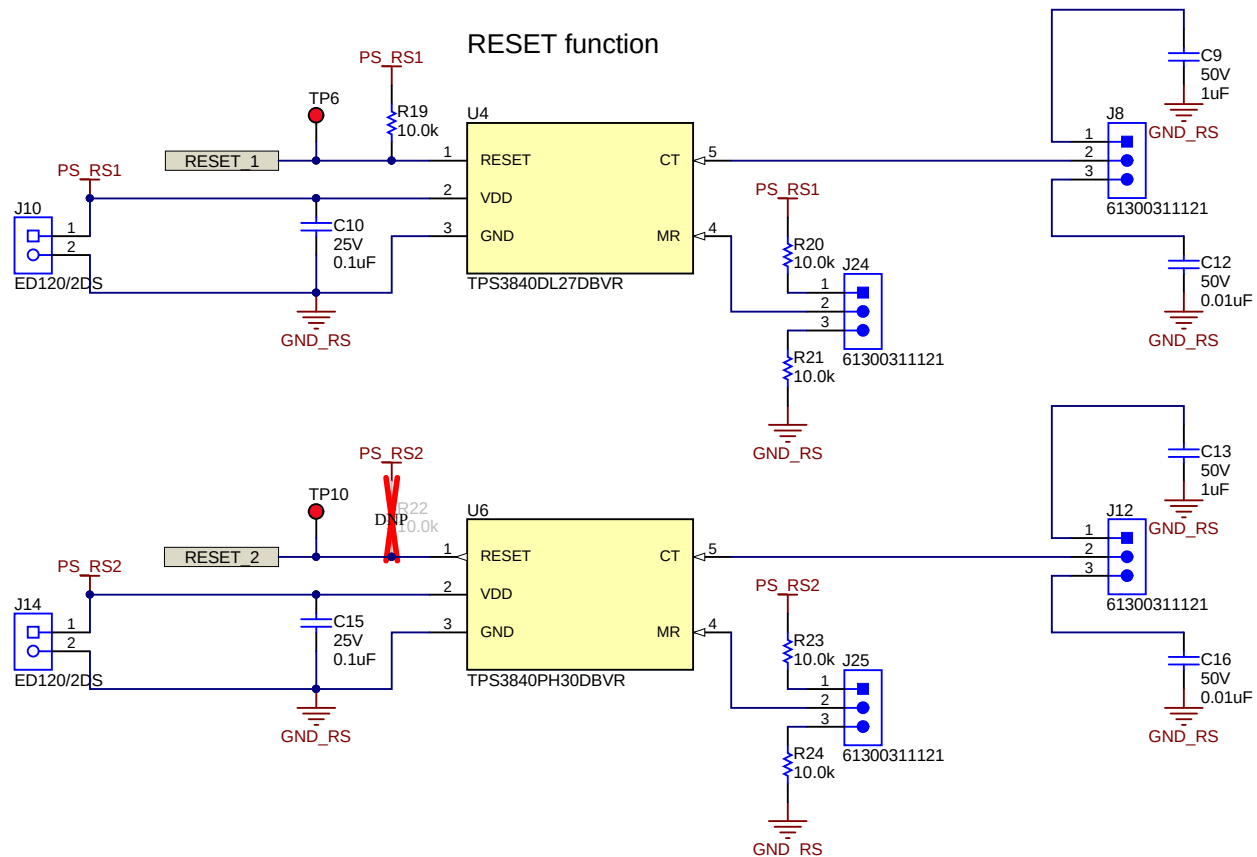
TIDA-010041



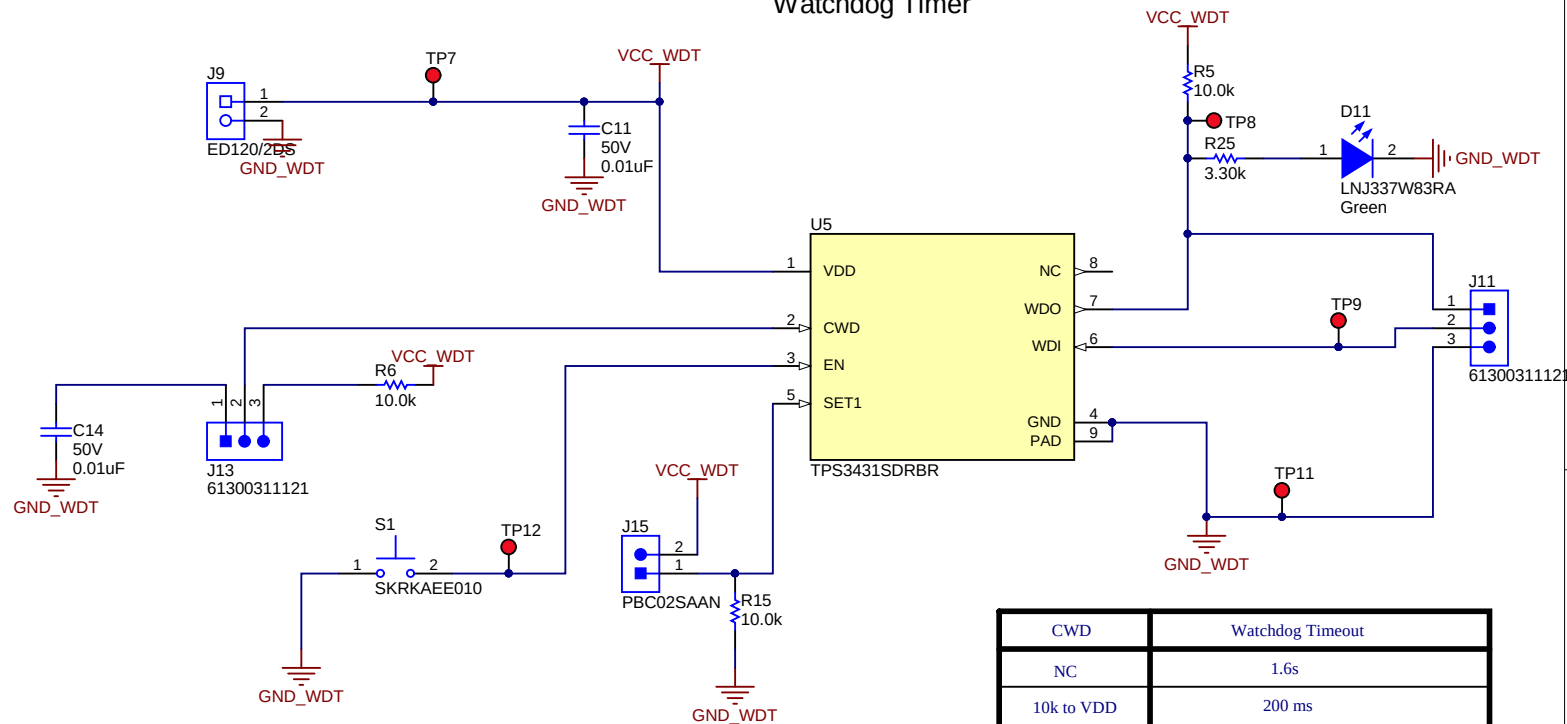
Power Sequencing



RESET function



Watchdog Timer



CWD	Watchdog Timeout
NC	1.6s
10k to VDD	200 ms
Cc wd	$t_{WD}(ms) = 77.4 \times CC_{WD}(nF) + 55 (ms)$

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Orderable: N/A	Designed for: Public Release	Mod. Date: 4/2/2019
TID #: 010041	Project Title: TIDA-010041	
Number: TIDA-010041	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 5
Drawn By:	File: TIDA-010041-E1 Power sequencing, Reset, WDT Schematic	Size: 11.0K
Engineer: Harsh Misra	Contact: http://www.ti.com/support	

Ground fault detection

A

B

A

B

C

D

C

D

Binary Voltage

Orderable: N/A	Designed for: Public Release	Mod. Date: 10/10/2018
TID #: 010041	Project Title: TIDA-010041	
Number: TIDA-010041	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 5
Drawn By: Harsh Misra	File: TIDA-010041-E1_Threshold detection.SchDoc	Size: B
Engineer: Harsh Misra	Contact: http://www.ti.com/support	

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